

LN4264T1G

N-Channel 60-V (D-S) MOSFET

1. FEATURES

- Low RDS(on) trench technology.
- Low thermal impedance.
- Fast switching speed.
- We declare that the material of product are Halogen Free and compliance with RoHS requirements.

2. APPLICATION

- DC/DC Conversion
- Motor Drives

3. ORDERING INFORMATION

Device	Marking	Shipping
LN4264T1G	LN4264	4000/Tape&Reel

4. MAXIMUM RATINGS(Ta = 25°C unless otherwise stated)

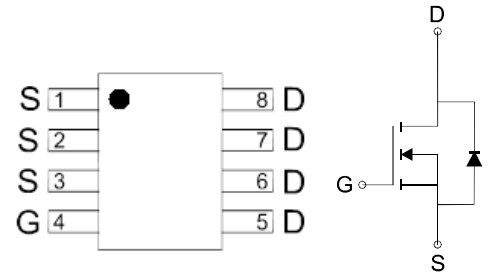
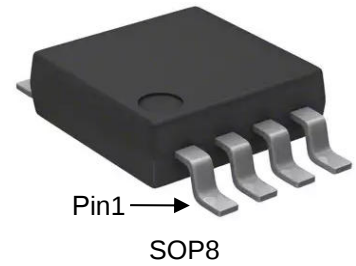
Parameter		Symbol	Limits	Unit
Drain-to-Source Voltage		VDSS	60	V
Gate-to-Source Voltage		VGS	±20	V
Continuous Drain Current(Note 1)	TA =25°C	ID	15	A
	TA =70°C		13	
Pulsed Drain Current (Note 2)		IDM	60	
Continuous Source Current (Diode Conduction)(Note 1)		IS	4.6	A
Power Dissipation(Note 1)	TA =25°C	PD	3.1	W
	TA =70°C		2.2	
Operating Junction Temperature		TJ	-55 ~+150	°C
Storage Temperature Range		Tstg	-55 ~+150	

1.Surface Mounted on 1" x 1" FR4 Board.

2.Pulse width limited by maximum junction temperature.

5. THERMAL CHARACTERISTICS

Parameter		Symbol	Limits	Unit
Maximum Junction-to-Ambient(Note 1)	t≤10s	RθJA	40	°C/W
	Steady State		80	
Maximum Junction-to-Case	Steady State	RθJC	26	



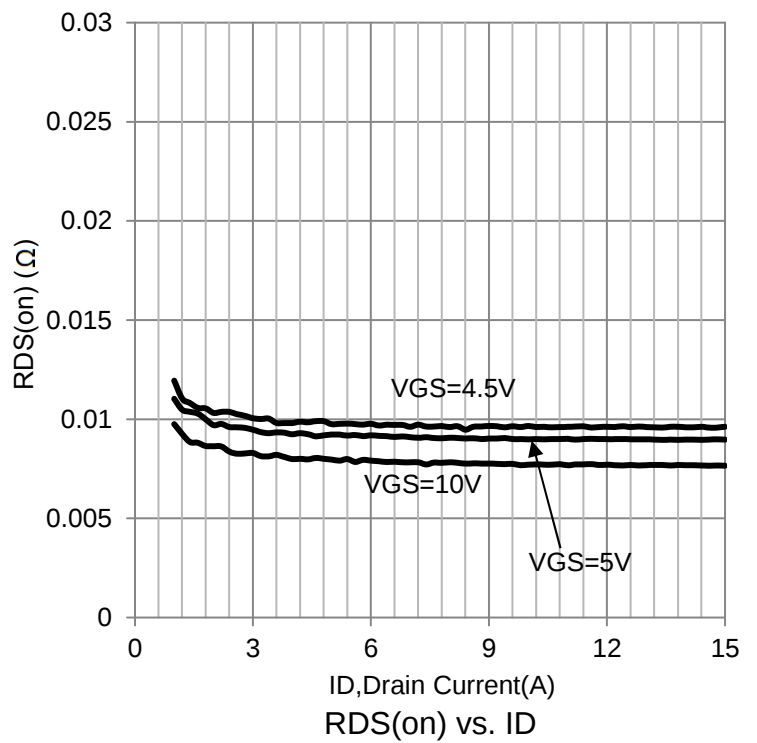
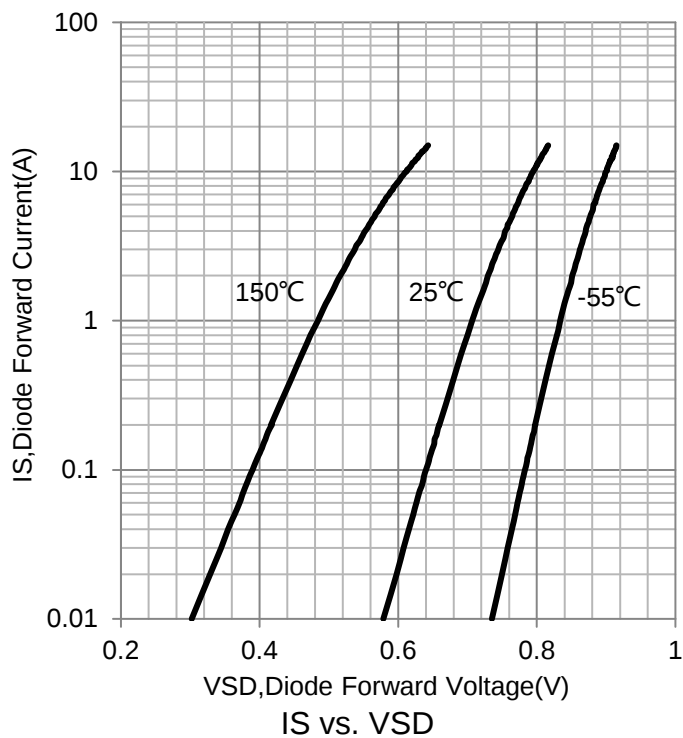
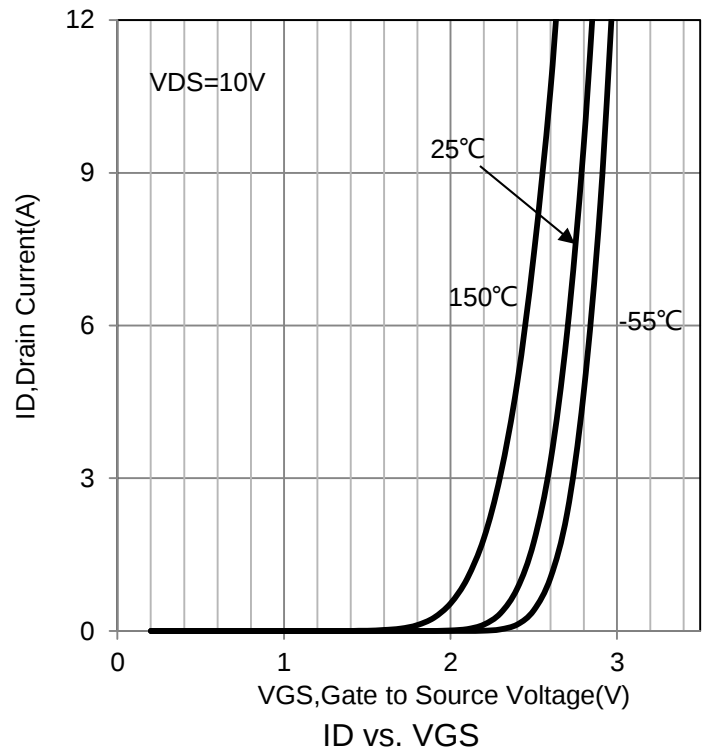
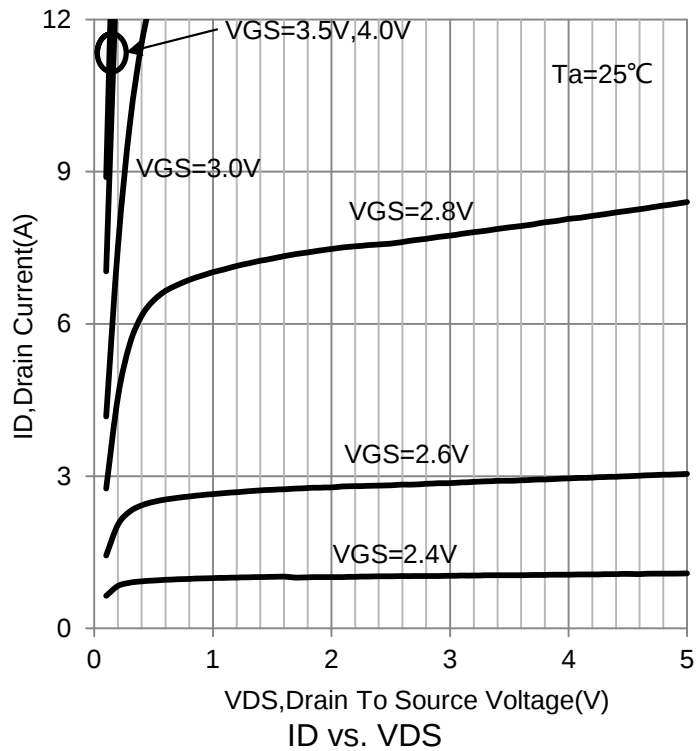
6. ELECTRICAL CHARACTERISTICS

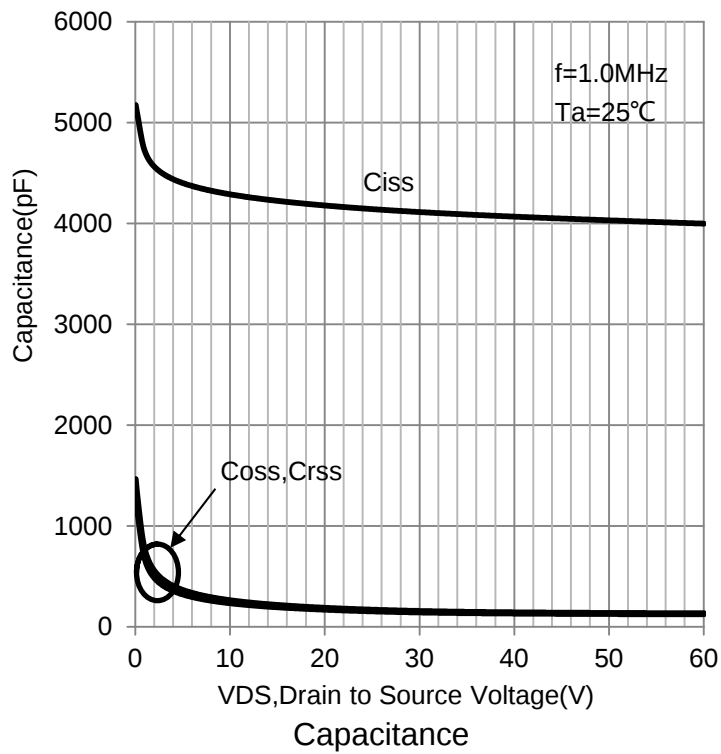
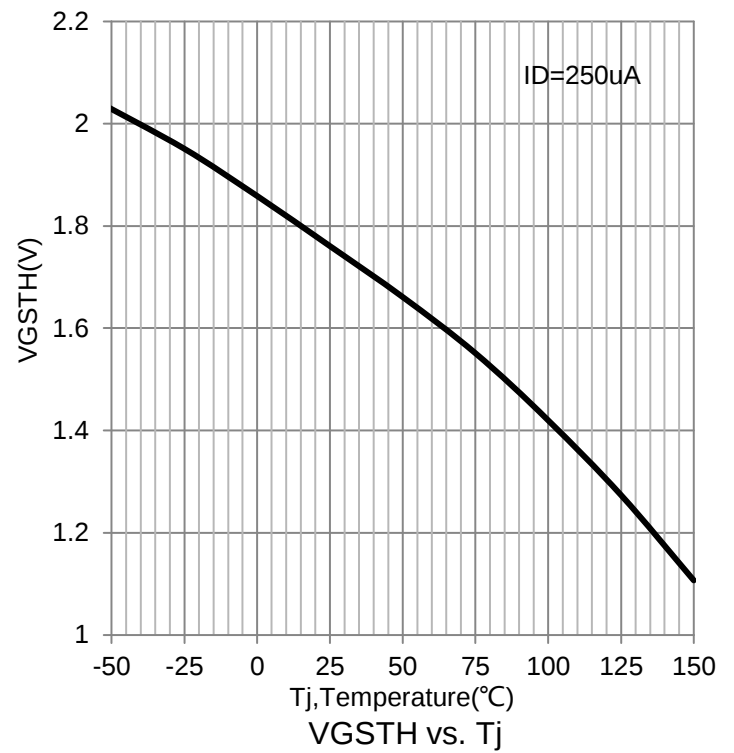
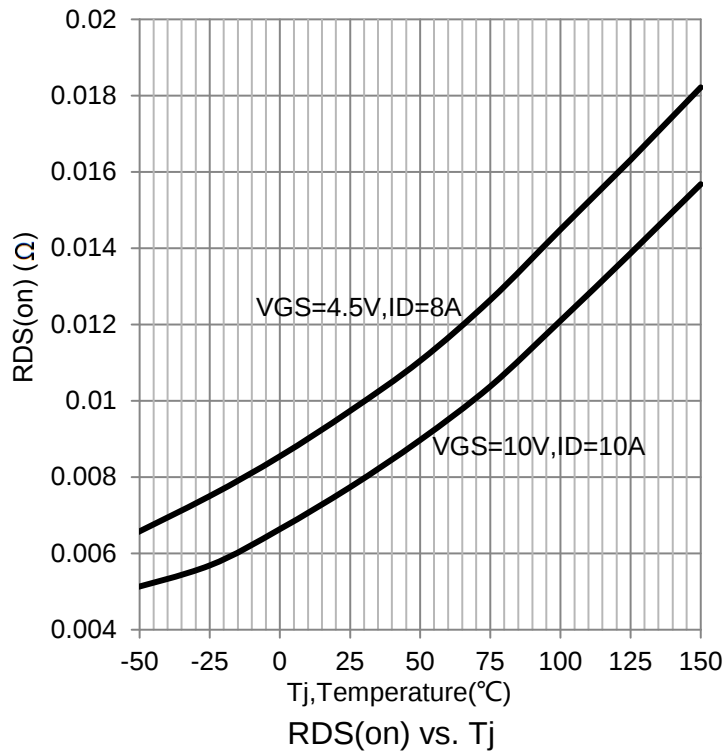
Characteristic	Symbol	Min.	Typ.	Max.	Unit
Static					
Gate-Source Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 250 \mu A$)	$V_{GS(th)}$	1	1.7	2.5	V
Gate-Body Leakage ($V_{DS} = 0 V$, $V_{GS} = \pm 20 V$)	I_{GSS}	-	-	± 100	nA
Zero Gate Voltage Drain Current ($V_{DS} = 48 V$, $V_{GS} = 0 V$) ($V_{DS} = 48 V$, $V_{GS} = 0 V$, $T_J = 55^\circ C$)	I_{DSS}	- -	- -	1 10	μA
On-State Drain Current(Note 3) ($V_{DS} = 5 V$, $V_{GS} = 10 V$)	$I_{D(on)}$	23	-	-	A
Drain-Source On-Resistance(Note 3) ($V_{GS} = 10 V$, $I_D = 10 A$) ($V_{GS} = 4.5 V$, $I_D = 8 A$)	$R_{DS(on)}$	- -	7.2 9.2	8.9 10.5	m Ω
Forward Transconductance(Note 3) ($V_{DS} = 15 V$, $I_D = 10 A$)	g_{fs}	-	53	-	S
Diode Forward Voltage(Note 3) ($I_S = 2.3 A$, $V_{GS} = 0 V$)	V_{SD}	-	0.74	-	V
Dynamic(Note 4)					
Total Gate Charge	(VDS = 30 V, VGS = 4.5 V, ID = 10 A)	Qg	-	32	nC
Gate-Source Charge		Qgs	-	9.2	
Gate-Drain Charge		Qgd	-	9.6	
Turn-On Delay Time	(VDS = 30 V, RL = 3 Ω , ID = 10 A, VGEN = 10 V, RGEN = 6 Ω)	td(on)	-	11	ns
Rise Time		tr	-	10	
Turn-Off Delay Time		td(off)	-	111	
Fall Time		tf	-	31	
Input Capacitance	(VDS = 15 V, VGS = 0 V, f = 1 MHz)	Ciss	-	4107	pF
Output Capacitance		Coss	-	222	
Reverse Transfer Capacitance		Crss	-	179	

3. Pulse test: $PW \leq 300 \mu s$ duty cycle $\leq 2\%$.

4. Guaranteed by design, not subject to production testing.

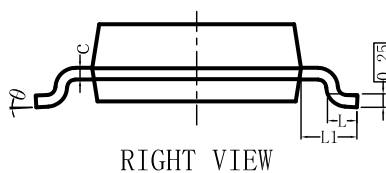
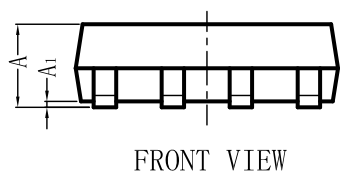
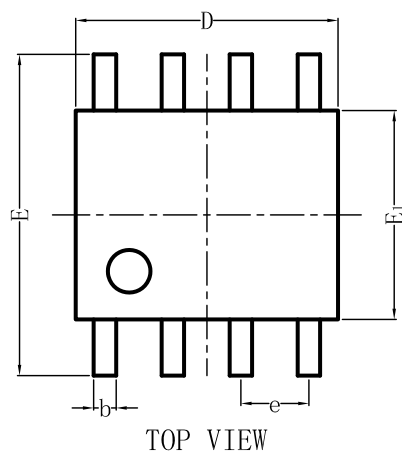
7. ELECTRICAL CHARACTERISTICS CURVES



7. ELECTRICAL CHARACTERISTICS CURVES(Con.)


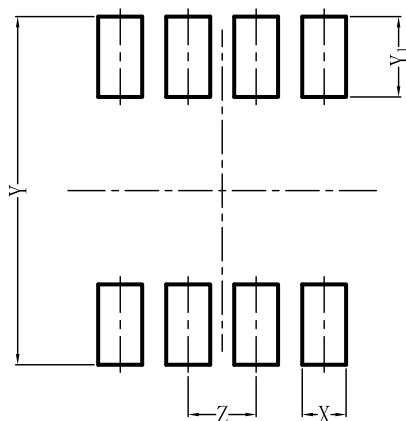
8.OUTLINE AND DIMENSIONS

SOP8



SOP8 (Unit:mm)			
Dim	Min	Typ	Max
A	1.35	1.55	1.75
A1	0.06	—	0.16
b	0.33	0.42	0.51
c	0.19	0.22	0.25
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27 BSC		
L	0.50	0.60	0.70
L1	1.05 REF		
θ	0°	—	8°

9.SOLDERING FOOTPRINT



Dimensions	(mm)
X	0.820
Y	6.500
Y1	1.500
Z	1.270

DISCLAIMER

- Curve guarantee in the specification. The curve of test items with electric parameter is used as quality guarantee. The curve of test items without electric parameter is used as reference only.
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